

O P E  
OCT 04 2004

U. S. PATENT & TRADEMARK OFFICE

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

B  
Btw

In re Patent Application of: )  
COFLER )  
Serial No. 10/083,629 ) Examiner: E. COLEMAN  
Confirmation No. 1789 ) Art Unit: 2183  
Filing Date: February 26, 2002 ) NOA Date: 9/2/04  
For: METHOD OF HANDLING INSTRUCTIONS)  
WITHIN A PROCESSOR WITH )  
DECOPLED ARCHITECTURE, IN )  
PARTICULAR A PROCESSOR FOR )  
DIGITAL SIGNAL PROCESSING, AND )  
CORRESPONDING PROCESSOR )  
)

AMENDMENT AFTER ALLOWANCE UNDER 37 C.F.R. 1.312

MS Issue fee  
Commissioner for Patents  
PO Box 1450  
Alexandria, VA 22313-1450

Sir:

Without withdrawing the above-identified application  
from allowance, please amend the application as follows: